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**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

<b>Application Number</b>	10816004	<b>Docket Number</b>	CNTR.2216
<b>Filed</b>	4/1/2004	<b>Group Art Unit</b>	2116
<b>Examiner</b>	ANAND PATEL	<b>Customer No.</b>	23669
<b>Application Title</b>	FREQUENCY-VOLTAGE MECHANISM FOR MICROPROCESSOR POWER MANAGEMENT		
<b>First Named Inventor</b>	DARIUS D. GASKINS		

**FACSIMILE COVER SHEET**

**To:** Commissioner for Patents - Central Fax Number  
**Fax Number:** 571-273-8300

**From:** HUFFMAN PATENT GROUP, LLC  
**Fax Number:** (661) 460-1986

**Pages:** 5 (including this cover sheet)

Dear Sir:

Please accept the attached correspondence for the above-identified matter. I hereby certify under 37 CFR 1.8 that this correspondence is being facsimile transmitted to the United States Patent and Trademark Office on the date of signature shown below.

Respectfully submitted,  
HUFFMAN PATENT GROUP, LLC

/ Richard K. Huffman /

By: \_\_\_\_\_

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04/06/2007

Date: \_\_\_\_\_